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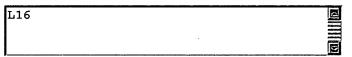US OCR Full-Text Database
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L16	14 and 11	6	L16
	DB=TDBD; PLUR=YES; OP=ADJ		
L15	CPu and processor$ and accelerat$ and stack$ and instruction$ and (updat$ or modif$ or chang$ or writ$ or alter) near8 variable$ and (virtual near4 machine$)and (sign$ near4 exten$) and operand$ and register$	0	L15
	DB=DWPI; PLUR=YES; OP=ADJ		
L14	CPu and processor$ and accelerat$ and stack$ and instruction$ and (updat$ or modif$ or chang$ or writ$ or alter) near8 variable$ and (virtual near4 machine$)and (sign$ near4 exten$) and operand$ and register$	0	L14
	DB=JPAB; PLUR=YES; OP=ADJ		
L13	CPu and processor$ and accelerat$ and stack$ and instruction$ and (updat$ or modif$ or chang$ or writ$ or alter) near8 variable$ and (virtual near4 machine$)and (sign$ near4 exten$) and operand$ and register$	0	L13
	DB=EPAB; PLUR=YES; OP=ADJ		

L12	CPu and processor$ and accelerat$ and stack$ and instruction$ and (updat$ or modif$ or chang$ or writ$ or alter) near8 variable$ and (virtual near4 machine$)and (sign$ near4 exten$) and operand$ and register$	0	L12

DB=PGPB; PLUR=YES; OP=ADJ

L11	CPu and processor$ and accelerat$ and stack$ and instruction$ and (updat$ or modif$ or chang$ or writ$ or alter) near8 variable$ and (virtual near4 machine$)and (sign$ near4 exten$) and operand$ and register$	1	L11

DB=USPT; PLUR=YES; OP=ADJ

L10	L9 and register$	6	L10
L9	L8 and operand$	6	L9
L8	L7 and (sipush or bipush)	6	L8
L7	L6 and (sign$ near4 exten$)	17	L7
L6	L5 and (program counter$ or pc)	70	L6
L5	L4 and (virtual near4 machine$)	88	L5
L4	CPu and processor$ and accelerat$ and stack$ and instruction$ and (updat$ or modif$ or chang$ or writ$ or alter) near8 variable$	330	L4
L3	L2	545	L3
L2	712/202,203,210.ccls.	545	L2
L1	717/139,136,140,118,148.ccls.	645	L1

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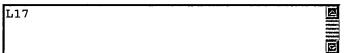| Terms	Documents
(717/136 |717/137 |717/139).ccls.	359

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L16	14 and 11	6	L16
	DB=TDBD; PLUR=YES; OP=ADJ		
L15	CPu and processor$ and accelerat$ and stack$ and instruction$ and (updat$ or modif$ or chang$ or writ$ or alter) near8 variable$ and (virtual near4 machine$)and (sign$ near4 exten$) and operand$ and register$	0	L15
	DB=DWPI; PLUR=YES; OP=ADJ		
L14	CPu and processor$ and accelerat$ and stack$ and instruction$ and (updat$ or modif$ or chang$ or writ$ or alter) near8 variable$ and (virtual near4 machine$)and (sign$ near4 exten$) and operand$ and register$	0	L14
	DB=JPAB; PLUR=YES; OP=ADJ		
L13	CPu and processor$ and accelerat$ and stack$ and instruction$ and (updat$ or modif$ or chang$ or writ$ or alter) near8 variable$ and (virtual near4 machine$)and (sign$ near4 exten$) and operand$ and register$	0	L13

DB=EPAB; PLUR=YES; OP=ADJ

L12 CPu and processor$ and accelerat$ and stack$ and instruction$ and (updat$ or modif$ or chang$ or writ$ or alter) near8 variable$ and (virtual near4 machine$)and (sign$ near4 exten$) and operand$ and register$ 0 L12

DB=PGPB; PLUR=YES; OP=ADJ

L11 CPu and processor$ and accelerat$ and stack$ and instruction$ and (updat$ or modif$ or chang$ or writ$ or alter) near8 variable$ and (virtual near4 machine$)and (sign$ near4 exten$) and operand$ and register$ 1 L11

DB=USPT; PLUR=YES; OP=ADJ

L10	L9 and register$	6	L10
L9	L8 and operand$	6	L9
L8	L7 and (sipush or bipush)	6	L8
L7	L6 and (sign$ near4 exten$)	17	L7
L6	L5 and (program counter$ or pc)	70	L6
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L3	L2	545	L3
L2	712/202,203,210.ccls.	545	L2
L1	717/139,136,140,118,148.ccls.	645	L1

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1 Techniques for obtaining high performance in Java programs
Iffat H. Kazi, Howard H. Chen, Berdenia Stanley, David J. Lilja
September 2000 **ACM Computing Surveys (CSUR)**, Volume 32 Issue 3

Full text available: 🗎 pdf(816.13 KB) Additional Information: full citation, abstract, references, citings, index terms

This survey describes research directions in techniques to improve the performance of programs written in the Java programming language. The standard technique for Java execution is interpretation, which provides for extensive portability of programs. A Java interpreter dynamically executes Java bytecodes, which comprise the instruction set of the Java Virtual Machine (JVM). Execution time performance of Java programs can be improved through compilation, possibly at the expense of portabili ...

Keywords: Java, Java virtual machine, bytecode-to-source translators, direct compilers, dynamic compilation, interpreters, just-in-time compilers

2 Exploiting Java instruction/thread level parallelism with horizontal multithreading
Kenji Watanabe, Wanming Chu, Yamin Li
January 2001 **Australian Computer Science Communications , Proceedings of the 6th Australasian conference on Computer systems architecture**, Volume 23 Issue 4

Full text available: 🗎 pdf(787.34 KB) Additional Information: full citation, abstract, references

Java bytecodes can be executed with the following three methods: a Java interpretor running on a particular machine interprets bytecodes; a Just-In-Time (JIT) compiler translates bytecodes to the native primitives of the particular machine and the machine executes the translated codes; and a Java processor executes bytecodes directly. The first two methods require no special hardware support for the execution of Java bytecodes and are widely used currently. The last method requires an embedded J ...

3 Bytecode fetch optimization for a Java interpreter
Kazunori Ogata, Hideaki Komatsu, Toshio Nakatani
October 2002 **Proceedings of the 10th international conference on Architectural support for programming languages and operating systems**, Volume 36 , 37 , 30 Issue 5 , 10 , 5

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